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APPLICANTS

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** CONTINUING DATA ****

This application is a CON of 09/511,620 02/23/2000 PAT 6,175,527
 which is a CON of 08/865,371 05/29/1997 PAT 6,055,655

** FOREIGN APPLICATIONS ****

JAPAN 8-136892 05/30/1996
 JAPAN 9-113900 05/01/1997

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 07/08/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 52	TOTAL CLAIMS 1	INDEPENDENT CLAIMS 1
Verified and Acknowledged Examiner's Signature	Initials				

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TITLE

Semiconductor integrated circuit device and method of testing the same

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